Live Course,
Hotel Starling @ EPFL, Lausanne, Switzerland

LOW-POWER ANALOG CIRCUIT DESIGN

JUNE 22-26, 2026

Monday, June 22		
08:30 - 12:00 am	MOS Transistor Modeling for Low-Voltage and Low-Power Circuit Design	Christian Enz
01:30 - 05:00 pm	Design of Low-Power Analog Circuits using the Inversion Coefficient	Christian Enz

Tuesday, June 23		
08:30 - 10:00 am	Noise Performance of Elementary Circuits	Boris Murmann
10:30 - 12:00 am	Noise Performance of Filters, Feedback & SC circuits	Boris Murmann
01:30 - 03:00 pm	Opamp Topologies and Design: Single-Stage Circuits	Boris Murmann
03:30 - 05:00 pm	Opamp Topologies: Cascoded and Two-Stage Circuits	Boris Murmann

Wednesday, June 24		
08:30 - 12:00 am	Power Dissipation in Analog Circuits	Klaas Bult
01:30 - 03:00 pm	Analog Design Methodology and Practical Techniques for Frequency Compensation	Vadim Ivanov
03:30 - 05:00 pm	Energy Efficient Voltage References, Biasing in Analog Systems and Current Sources	Vadim Ivanov

Thursday, June 25		
08:30 - 10:00 am	Power Dissipation in ADC Buidling Blocks	Klaas Bult
10:30 - 12:00 am	Power Dissipation in ADCs	Klaas Bult
01:30 - 05:00 pm	Micropower ADCs	Kofi Makinwa

Friday, June 26		
08:30 - 12:00 am	Energy Efficient sensor Interfaces	Taekwang Jang
01:30 - 03:00 pm	Low-Power Frequency Reference Circuits	Taekwang Jang
03:30 - 05:00 pm	Power Management with Nanoampere Consumption and Efficient Energy Harvesting	Vadim Ivanov